

List of Reference Symbols

1	Carrier substrate
2, 2A, 2B	Insulation layer
3	Semiconductor component layer
3S	Splitting-off boundary layer
4	Gate dielectric
5	Control layer
6	Covering layer
7	Source/drain regions
8	Spacer
9	Connecting layer
10	Intermediate insulation layer
11	Bitline contact
12	Bitline layer
P	Depression, pores
E1	Capacitor counterelectrode
D	Dielectric layer
E2	Capacitor electrode
S	Semiconductor substrate
M	Mask layer
V, V1, V2	Contact hole
PK	(Group) Capacitor
WL	Wordline
BL	Bitline